



Figure 4-1. Data Address Generator (DAG) Block Diagram

i On previous 16-bit, fixed-point DSPs (ADSP-218x family), the DAG registers are 14-bits wide, instead of 16-bits wide as on the ADSP-219x DSPs. Because the ADSP-219x DAG registers are 16-bits wide, the DAGs do not need to perform the zero padding on I and L register writes to memory or the sign extension on M register writes to memory that is required for previous ADSP-218x family DSPs.